

ELEC3104

Digital Signal Processing

Course Outline – Semester 1, 2017

Never Stand Still

Faculty of Engineering

School of Electrical Engineering and Telecommunications

Course Staff

Course Convener: Dr Vidhyasaharan Sethu, MSEB 649, v.sethu@unsw.edu.au

Tutorial-Lab Coordinator: Saad Irtza, s.irtza@unsw.edu.au

Project Coordinator: Jeremy Bell, jeremy.bell@unsw.edu.au

Consultations: You are encouraged to ask questions on the course material, after the lecture class times in the first instance, rather than via email. Any email enquiry regarding the course should be made from your student email address with ELEC3104 in the subject line; otherwise they will not be answered. All tutorial-lab related emails should be directed to Saad Irtza in the first instance and all project related queries should be directed to Jeremy Bell.

Keeping Informed: Announcements may be made during classes, via email (to your student email address) and/or via online learning and teaching platforms – in this course, we will use Moodle <https://moodle.telt.unsw.edu.au/login/index.php>. Please note that you will be deemed to have received this information, so you should take careful note of all announcements.

Course Summary

Contact Hours

The course consists of 3 hours of lectures, and a 3-hour of integrated tutorial-lab each week.

	Day	Time	Location	Name
Lectures	Tuesday	11:00-13:00	Ritchie Th.	Dr. V. Sethu
	Thursday	16:00-17:00	ChemSc M17	
Tutorial-Labs	Mon - Fri	Enrolled times	EE302 / EE214	Lab Demonstrators
Consultations	Mon – Fri	Allocated time	Via Moodle	Lab Demonstrators

Context and Aims

Signal Processing is the process of measuring, manipulating or analysing information. Signals of interest include biomedical data, audio, still or moving images, radar, and even DNA. Filtering techniques can be crucial in revealing and interpreting information present in a signal. ELEC3104 Digital Signal Processing is an introductory signal processing course which takes students through the steps necessary to design and implement filters for a range of signals.

Aims

The course aims to equip students with:

- An understanding of the behaviour of a system, in terms of both its time domain and frequency domain representations.
- The skills to identify the correct type of filter required for a given problem and be able to demonstrate the design and implementation of a digital filter.
- The ability to explain the concept of aliasing and its effect on the design of practical systems.
- An understanding of multi-rate processing and multi-rate systems.

Indicative Lecture Schedule

The indicative lecture schedule is listed in the following table.

Week	Day	Lecture	Laboratory
1	Tuesday	Introduction to DSP and understanding Signals	Self-Study – MATLAB
	Thursday		
2	Tuesday		
	Thursday		
3	Tuesday	Analysis of Discrete Systems	Tutorial Lab
	Thursday		
4	Tuesday		
	Thursday		Tutorial Lab
5	Tuesday	Discrete Fourier Transform	Project Lab (Optional)
	Thursday		
6	Tuesday		
	Thursday		Tutorial Lab
7	Tuesday	Filter Design	Tutorial Lab
	Thursday		
8	Public Holiday		
	Thursday		Project Lab (Optional)
9	Tuesday		
	Thursday	Project Assessment	
10	Tuesday	Peer assessment of assignment	Tutorial Lab
	Thursday	Filter Design	
11	Tuesday	Multi-rate Processing	Project Lab (Optional)
	Thursday		
12	Tuesday		
	Thursday		Tutorial Lab
13	Tuesday		Project Assessment
	Thursday		

Note:

1. MATLAB Tutorial Videos: <http://eemedia.ee.unsw.edu.au/MatlabTutorial/index.htm>
2. There are 3 tutorial-lab sheets –they will be released in week 3.
3. The project will be released in week 3.

Assessment

Tutorial-labs	10%
Project	25%
Assignment	15%
Final Exam (2 hours)	50%

Course Details

Credits

This is a 6 UoC course and the expected workload is 10-12 hours per week throughout the 13 week semester.

Relationship to Other Courses

This is a 3rd year course in the School of Electrical Engineering and Telecommunications at the University of New South Wales. It is a core course for students following a BE (Electrical) or (Telecommunications) program and other combined degree programs, and an elective for Computer Engineering students.

Pre-requisites and Assumed Knowledge

The pre-requisite for this course is ELEC2134, Circuits and Signals. It is essential that students are familiar with basic circuit theory, signal analysis and transform methods. It is further assumed that students are familiar with the MATLAB environment, and have good computer literacy.

Following Courses

The course is a pre-requisite for all professional electives in the Signal Processing group, including ELEC4621 Advanced Digital Signal Processing and ELEC4622 Multimedia Signal Processing

Learning outcomes

At the end of the course students should:

1. Be able to apply transform methods to the analysis of analogue and digital linear time-invariant systems
2. Develop the appropriate competency in converting between time and frequency domain representations of signals and systems
3. Understand the practical aspects of sampling and reconstruction and be able to select a suitable sampling rate for a given signal processing problem
4. Design and analyse analogue and digital filters for a given specification
5. Demonstrate an understanding of the use and applications of the Discrete Fourier transform
6. Have gained practical experience with the implementation of digital filters
7. Be able to implement a simple multi-rate system

This course is designed to provide the above learning outcomes which arise from targeted graduate capabilities listed in **Appendix A**. This course also addresses the Engineers Australia (National Accreditation Body) Stage I competency standard as outlined in **Appendix B**.

Syllabus

Processing and analysis of continuous (analogue) and discrete-time (digital) signals. Sampling continuous signals: the sampling theorem, reconstruction, aliasing and the z-transform. Analogue filters: Butterworth filters. Filter impulse and frequency responses,

stability and digital oscillators. The Discrete Fourier Transform (DFT). Fundamentals of the design and realisation of finite impulse response (FIR) and infinite impulse response (IIR) digital filters. Linear and non-linear phase filters. Decimation, interpolation, multi-rate digital signal processing.

Teaching Strategies

Delivery Mode

This entire course will be delivered via live lectures. Lecture notes of mathematical concepts and relationships will also be provided to complement the lectures. Please note that the notes and the lectures are designed to be complementary, you should not treat the notes as a replacement for lectures. Sample problem solutions previously recorded by Prof. Ambikairajah will also be provided to support these lectures.

Learning in this course

1. You are expected to attend all lectures, tutorial-labs, and project assessments in order to maximise learning.
2. You must prepare well for your tutorial-lab classes.
3. Reading additional texts will further enhance your learning experience.
4. Group learning is also encouraged.
5. You are encouraged to discuss your project design with your peers but you should implement the project on your own.
6. Similarly group discussion of the assignment is encouraged but individual completion of the assignment is a requirement.
7. UNSW *assumes* that self-directed study of this kind is undertaken in addition to attending face-to-face classes throughout the course.

Tutorial-Laboratory classes

The integrated tutorial-laboratory sessions are designed to help you develop your analytical skills and see how they are applicable in a practical context. You may divide your time between the analytical and the laboratory components as per your convenience but you should complete both within the allocated time. The analytical problems you will be given in these sessions will tend to be more involved than the sample problems in the recommended textbook and will also tend to involve more than one topic. **It is expected that you are able to solve the sample problems at the end of each chapter and the recommended text book prior to undertaking the tutorial-lab questions.**

A total of 3 tutorial-laboratory question sheets will have to be completed in this course and are designed to provide practical, hands-on exposure to the concepts conveyed in lectures soon after they are covered in class. Completion of these tutorial-laboratory problems will be marked off by your laboratory demonstrators and will form the basis of your tutorial-lab mark for the course. While you are free to complete these 3 sheets in your own time between week 3 and week 11, you should complete them in order. Further, **it is strongly encouraged that you complete tutorial-lab sheet 1 by week 4, tutorial-lab sheet 2 by week 7 and tutorial-lab sheet 3 by week 12.** . Laboratory attendance WILL be kept, and you MUST attend all tutorial-labs.

Project Laboratory classes

The scheduled project labs are an opportunity for you to discuss your project implementation with a laboratory demonstrator. The project is designed to provide a hands-on exposure to the applications of the concepts learnt in the course in implementing a DSP system. While attendance to project labs is optional (except for project assessments which you should attend) you are strongly encouraged to attend them and discuss your implementation with

your lab demonstrators to complement your self-directed learning. **The project will be assessed in two stages, in week 9 and week 13 and attendance to these project assessment labs is compulsory.**

Tutorial-Laboratory Exemption

There is no laboratory exemption for this course. Regardless of whether equivalent labs have been completed in previous courses, all students enrolled in this course for Session 1, 2017 must take the labs. If, for medical reasons, (note that a valid medical certificate must be provided) you are unable to attend a lab, please meet the co-ordinator (Saad Irtza) to discuss the situation.

Pre-requisite to pass the course

A satisfactory performance (50% or greater) in **each** of the following, is a necessary requirement to pass this course:

- **Assignment**
- **Project**
- **Final Exam**

Assessment

The assessment scheme in this course reflects the intention to assess your learning progress through the semester. Ongoing assessment occurs through completion of tutorial-lab sheets, the two stages of the project and the peer assessed assignment.

Tutorial-labs (completion – ongoing)	10%
Project (Week 9 and Week 13)	25%
Assignment (Peer assessed)	15%
Final Exam (2 hours)	50%

Tutorial-Lab Sheets (10%)

You are required to complete a total of 3 tutorial-lab sheets and you will have to be marked off by the demonstrators on completion. You can do these at your own pace, but it is recommended that you complete sheet 1 by week4, sheet 2 by week 7 and sheet 3 by week 12. **Note, you can only get the completion of one sheet marked off in any one week so plan accordingly (You cannot get them all marked off in the final week).**

Project (25%)

The project will comprise of two stages. The first stage will be evaluated in week 9 and the second stage in week 13. At both stages, your project will be assessed by a pair of lab demonstrators in terms of both your implementations (based on a working demonstration) and your understanding (based on your verbal answers to questions from the demonstrators). **You must pass the project to pass the course.**

Assignment (15%)

The assignment will be given in week 4 and should be handed back in week 10. It will comprise of a set of analytical questions and is designed to be challenging. The assignment will be peer-assessed in the lecture in week 10 (9th May). Attendance to this lecture is compulsory and your assignment will not be marked if you are absent. Group discussions are encouraged when completing the assignment but completed assignments should be submitted individually. If for medical reasons, (note that a valid medical certificate must be provided) you are unable to attend this lecture, you will be given an alternative assessment (either oral or written) at the discretion of the course convener. **You must pass the assignment to pass the course.**

Final Exam (50%)

There will be one final examination, testing your understanding of the principles and your analytical skills through a number of set problems. If for medical reasons, (note that a valid medical certificate must be provided to the university) you are unable to attend the final exam, you will be given another exam (either oral or written, at the discretion of the course convenor). **You must pass this final exam to pass the course.**

- The final exam will be 2 hours long
- The final exam will cover all chapters covered in the semester

Relationship of Assessment Methods to Learning Outcomes

Assessment	Learning outcomes						
	1	2	3	4	5	6	7
Tutorial-Lab completion (10%)	✓	✓	✓	✓	✓	✓	✓
Project (25%)	✓	✓	✓	✓	✓	✓	-
Assignment (15%)	✓	✓	✓	✓	✓	-	✓
Final examination (50%)	✓	✓	✓	✓	✓	-	✓

Course Resources

Textbooks

Prescribed textbook

S. K. Mitra, Digital Signal Processing, McGraw-Hill, 2011. This book is available at the UNSW bookshop and also available as an ebook - <https://create.mheducation.com/shop/#/catalog/details/?isbn=9781308454726>

Reference books

- R. G. Lyons, Understanding Digital Signal Processing, 3rd ed, Prentice-Hall, 2011
- J. Proakis & D. Manolakis, Digital Signal Processing, Prentice-Hall, 2007.
- A. V. Oppenheim, R. W. Schaffer, & P. Buck, Discrete-Time Signal Processing, Prentice-Hall, 2010.

On-line resources

Moodle

As a part of the teaching component, Moodle will be used to disseminate teaching materials, host forums and occasionally quizzes. Assessment marks will also be made available via Moodle: <https://moodle.telt.unsw.edu.au/login/index.php>.

Mailing list

Announcements concerning course information will be given in the lectures and/or on Moodle and/or via email (which will be sent to your student email address).

Other Matters

Academic Honesty and Plagiarism

Plagiarism is the unacknowledged use of other people's work, including the copying of assignment works and laboratory results from other students. Plagiarism is considered a form of academic misconduct, and the University has very strict rules that include some severe penalties. For UNSW policies, penalties and information to help you avoid plagiarism, see <https://student.unsw.edu.au/plagiarism>. To find out if you understand plagiarism correctly, try this short quiz: <https://student.unsw.edu.au/plagiarism-quiz>.

Student Responsibilities and Conduct

Students are expected to be familiar with and adhere to all UNSW policies (see <https://student.unsw.edu.au/guide>), and particular attention is drawn to the following:

Workload

It is expected that you will spend at least **ten to twelve hours per week** studying a 6 UoC course, from Week 1 until the final assessment, including both face-to-face classes and *independent, self-directed study*. In periods where you need to need to complete assignments or prepare for examinations, the workload may be greater. Over-commitment has been a common source of failure for many students. You should take the required workload into account when planning how to balance study with employment and other activities.

Attendance

Regular and punctual attendance at all classes is expected. UNSW regulations state that if students attend less than 80% of scheduled classes they may be refused final assessment.

General Conduct and Behaviour

Consideration and respect for the needs of your fellow students and teaching staff is an expectation. Conduct which unduly disrupts or interferes with a class is not acceptable and students may be asked to leave the class.

Work Health and Safety

UNSW policy requires each person to work safely and responsibly, in order to avoid personal injury and to protect the safety of others.

Special Consideration and Supplementary Examinations

You must submit all assignments and attend all examinations scheduled for your course. You should seek assistance early if you suffer illness or misadventure which affects your course progress. All applications for special consideration must be **lodged online through myUNSW within 3 working days of the assessment**, not to course or school staff. For more detail, consult <https://student.unsw.edu.au/special-consideration>.

Continual Course Improvement

This course is under constant revision in order to improve the learning outcomes for all students. Please forward any feedback (positive or negative) on the course to the course convener or via the myExperience Process. You can also provide feedback to ELSOC who will raise your concerns at student focus group meetings.

As a result of previous feedback obtained for this course (from 2015-2016) and in our efforts to provide a rich and meaningful learning experience, we have:

1. Introduced a project component to provide additional hands-on learning of implementation of DSP systems

2. Replaced the mid-semester exam with a peer-assessed assignment to encourage deeper learning of analytical concepts.
3. Provided additional MATLAB resources including a series of videos
4. Assigned a lab demonstrator for each group of 18 students and have made them available outside of tutorial-lab hours for an additional hour of consultation each week and to be able to provide personalized feedback and support for the tutorial-labs and the assignment.

Administrative Matters

On issues and procedures regarding such matters as special needs, equity and diversity, occupational health and safety, enrolment, rights, and general expectations of students, please refer to the School and UNSW policies:

<http://www.engineering.unsw.edu.au/electrical-engineering/policies-and-procedures>

<https://my.unsw.edu.au/student/atoz/ABC.html>

Important Points

Please note the following:

- During your tutorial-labs, a lab demonstrator will be present and will be able to guide you in your laboratory-based learning.
- MATLAB resources will be made available on the course Moodle.
- Guidelines on learning that inform teaching at UNSW are available at www.guidelinesonlearning.unsw.edu.au

Appendix A: Targeted Graduate Capabilities

Electrical Engineering and Telecommunications programs are designed to address the following targeted capabilities which were developed by the school in conjunction with the requirements of professional and industry bodies:

- The ability to apply knowledge of basic science and fundamental technologies;
- The skills to communicate effectively, not only with engineers but also with the wider community;
- The capability to undertake challenging analysis and design problems and find optimal solutions;
- Expertise in decomposing a problem into its constituent parts, and in defining the scope of each part;
- A working knowledge of how to locate required information and use information resources to their maximum advantage;
- Proficiency in developing and implementing project plans, investigating alternative solutions, and critically evaluating differing strategies;
- An understanding of the social, cultural and global responsibilities of the professional engineer;
- The ability to work effectively as an individual or in a team;
- An understanding of professional and ethical responsibilities;
- The ability to engage in lifelong independent and reflective learning.

Appendix B: Engineers Australia (EA) Professional Engineer Competency Standard

	Program Intended Learning Outcomes	
PE1: Knowledge and Skill Base	PE1.1 Comprehensive, theory-based understanding of underpinning fundamentals	✓
	PE1.2 Conceptual understanding of underpinning maths, analysis, statistics, computing	✓
	PE1.3 In-depth understanding of specialist bodies of knowledge	✓
	PE1.4 Discernment of knowledge development and research directions	✓
	PE1.5 Knowledge of engineering design practice	
	PE1.6 Understanding of scope, principles, norms, accountabilities of sustainable engineering practice	
PE2: Engineering Application Ability	PE2.1 Application of established engineering methods to complex problem solving	✓
	PE2.2 Fluent application of engineering techniques, tools and resources	✓
	PE2.3 Application of systematic engineering synthesis and design processes	
	PE2.4 Application of systematic approaches to the conduct and management of engineering projects	
PE3: Professional and Personal Attributes	PE3.1 Ethical conduct and professional accountability	
	PE3.2 Effective oral and written communication (professional and lay domains)	✓
	PE3.3 Creative, innovative and pro-active demeanour	
	PE3.4 Professional use and management of information	
	PE3.5 Orderly management of self, and professional conduct	
	PE3.6 Effective team membership and team leadership	